IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: K. TSUCHIDA

Serial No. 10/086,691 Filed: March 4, 2002

Group Art:

2812

Examiner:

A. Roman

For:

MOLD CLEANING SHEET AND MANUFACTURING

METHOD OF A SEMICONDUCTOR DEVICE

USING THE SAME

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Mail Stop Patent Application P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, the Applicants inform the Examiner of the documents cited during prosecution of the parent application, USSN 10/086,691.

The Applicants request the Examiner to initial and return a copy of the attached PTO-1449 form as an indication that the references have been considered.

Respectfully submitted

Shrinath Malur

Registration No. 34,663 Attorney for Applicant(s)

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Date: January 26, 2004

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